As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled

INTEGRATED CIRCUITS WITH SCALABLE DESIGN

which (check)		is attached hereto. and is amended by the l was filed on as Applic and was amended on			
I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.					
I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.					
I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:					
Include Provisional Application Number If Applicable					
Provisional Application Number			Filing Date		
	N/A	A			

I hereby appoint the following practitioners to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Customer Number 27869

27869

PATENT TRADEMARK OFFICE

Attorney Docket No.: M-12366 US

Please address all correspondence and telephone calls to:

N. R. H. Black

SKJERVEN MORRILL MacPHERSON LLP

Three Embarcadero Center, 28th Floor San Francisco, California 94111

Telephone:

415-217-6000

Facsimile:

415-434-0646

I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or

Sec. 9. 11 speed

entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

Full name of first joint Inventor's Signature:	inventor: Hamid Reza Rategh	Date:	11,30/01			
Residence:	Cupertino, California	_				
Post Office Address:	5612 Stevens Creek Blvd., Apt. #231	Citizenship:	Iran			
	Cupertino, CA 95014					
Full name of second joint inventor: Mehdi Frederik Soltan						
Inventor's Signature:	·	Date:	11/30/01			
Residence:	Stanford, California	_				
Post Office Address:	93D Escondido Village	Citizenship:	Denmark			
	Stanford CA 9/305					